

## **System Requirements for Memories**

Instructor: Rob Aitken, ARM Fellow at ARM Research

- Limitations of current memory technologies for systems
- Including also discussion SRAM
- Storage Class Memories
- Requirements for future memories for system point of view

### **Rob Aitken**

Rob Aitken is an ARM Fellow responsible for technology direction at ARM Research. He works on low power design, library architecture for advanced process nodes, technology roadmapping, and next generation memories. His group has participated in numerous chip tape-outs, including 8 at or below the 16nm node. He has published over 80 technical papers, on a wide range of topics.

Dr. Aitken joined ARM as part of its acquisition of Artisan Components in 2004. Prior to Artisan, he worked at Agilent and HP. He holds a Ph.D. from McGill University in Canada